

RESEARCH

Open Access

Fast performance prediction of power controlled HSUPA with channel estimation

Mohamed Et-tolba^{1*}, Samir Saoudi² and Raphaël Visoz³

Abstract

Transmit power control (TPC) is used in high speed uplink packet access (HSUPA) to compensate for the near far effect which degrades system performance. However, its use in joint application with turbo coding, and hybrid automatic repeat request (Hybrid-ARQ) is very prohibitive and time consuming. In this article, we propose a simplified simulation methodology for power controlled HSUPA with Hybrid-ARQ Chase combining considering the effect of channel estimation on the system performance. The proposed method was tested on Rake and chip-level linear minimum mean squared error (LMMSE) receivers. Simulation results show that the CPU time taken to reach the required performance is significantly reduced. Moreover, when the channel estimation is taken into account with an important number of pilot symbols, the system performance is close to that obtained with perfect channel acknowledgement (PCA).

Keywords: HSUPA, Hybrid-ARQ, chase combining, TPC, LMMSE, channel estimation, pilot symbols

1. Introduction

Transmit power control is necessary for high speed uplink packet access (HSUPA) system to reach the expected quality of service. It is jointly performed with Hybrid-ARQ in which ARQ technique is combined with turbo coding. Furthermore, adaptive modulation and coding, and the multicode transmission principle are used to provide high data rates. System performance, which is determined by the block error rate (BLER), depends on selected technology (channel coding, interleaving, modulation, and channel impulse response). The BLER is computed by simulating HSUPA technologies, according to 3GPP (3rd Group Partnership Project), with Monte-Carlo method [1]. However, iterative processes such as turbo decoding and transmitted power adjustment make this simulation very prohibitive and time consuming. As a solution, performance prediction has been proposed in many studies. This prediction aims to give an abstraction of the system performance over a multipath channel by summarizing the turbo code performance as look-up tables (reference curves). In [2], Kim et al. used a convex metric method that

maps the received signal to noise ratio (SNR) to a Gaussian capacity. Its disadvantage is that it requires a significant memory storage since it uses more than one reference curve as shown in [3]. The method we have proposed in [4] considers one reference curve assuming Gaussian assumption (GA) at the detector output. Furthermore, it assumes perfect channel state knowledge at the receiver side. The GA assumption is also used in [5] for extending the fast performance prediction (FPP) to iterative interference cancellation in multi-user MIMO CDMA. In [6], a prediction method has been proposed for coded MIMO-OFDM systems.

This article deals with a FPP for HSUPA system, considering TPC in joint application with Hybrid-ARQ Chase combining. In our methodology, the focus is put on the channel estimation effect on the prediction process. In addition, we try to assess the TPC robustness against a noisy channel.

Under block fading frequency selective channel hypothesis, and GA assumption on the detector output, the analytical signal to interference-plus-noise ratio (SINR) is equivalent to the SNR when the transmission is done through an additive white Gaussian noise (AWGN) channel. Hence, for HSUPA performance prediction over block fading frequency selective channel, FPP makes use of the analytical SINR with previously

* Correspondence: mohamed.ettolba@gmail.com

¹Department of Communications Systems, INPT, 2 Av. Allal El Fassi, Madinat Al Irfane, Rabat, Morocco

Full list of author information is available at the end of the article

stored look-up tables (LUT). These tables are built with a turbo coded HSUPA simulator over AWGN channel. The rest of this article is organized as follows: HSUPA system model is described in Section 2. The FPP with TPC and Hybrid-ARQ Chase combining is introduced in Section 3. The impact of channel estimation on performance prediction is presented in Section 4. Simulation results and the conclusion are given in Sections 5 and 6, respectively.

2. HSUPA system model

2.1. HSUPA transmitter

As shown in Figure 1, the user data stream is transmitted with K parallel enhanced dedicated physical data channels (E-DPDCHs). The control information need for data detection is carried by the newly introduced enhanced dedicated physical control channel (E-DPCCH). The HSUPA control channel (HS-DPCCH) is used for transmitting the channel state to the receiver (node-B). The enhanced data channels are always time-multiplexed with the uplink control channel, DPCCH, which carries known pilot bits for channel estimation, and signal to interference ration (SIR) estimation purposes at the receiver side. A part of transmitted control information on the DPCCH is reserved for downlink power control as shown on the frame structure of DPCCH which is presented in Figure 2.

The spreading process is performed using orthogonal variable spreading factor (OVSF) codes. The code length is different from one physical channel to another. In order to compensate for this difference, the signal on each channel is weighted by a gain factor: $\beta_{ed,k}$ for k th E-DPDCH, β_{ec} for E-DPCCH, β_{hs} for HS-DPCCH, and β_c for DPCCH. Then, the IQ mapping is done with iq coefficient which takes the value j for Q branch mapping, or 1 for I branch mapping. After IQ multiplexing, the complex-valued multicode chip sequence is scrambled with the long scrambling code $S_{dpdch,n}$. The scrambled multicode chip is expressed as

$$x_m = \sqrt{E_c} \sum_{k=0}^{K-1} s_k \left(\left\lfloor \frac{m}{SF_{ed,k}} \right\rfloor \right) c_{ed,k} \left\lfloor \frac{m}{SF_{ed,k}} \right\rfloor \left(m - \left\lfloor \frac{m}{SF_{ed,k}} \right\rfloor SF_{ed,k} \right) \cdot \beta_{ed,k} \cdot iq_{ed,k}, \quad (2.1)$$

where E_c is the chip energy, $c_{ed,k}$ is the spreading code, of length $SF_{ed,k}$, used on the k th E-DPDCH.

2.2. Sliding window model

Assume that the transmission is made through a multipath frequency selective channel, of length L , represented by a matrix \mathbf{H} , and consider a sliding window of length $2 \times E + 1$. The received signal, associated to the m th transmitted multicode chip is given by

$$\mathbf{y}_m = \mathbf{H}\mathbf{x}_m + \mathbf{n}_m, \quad (2.2)$$

When assuming receive diversity with N_r antennas, the received signal is expressed according to this sliding window model

$$\begin{pmatrix} \mathbf{y}_m^1 \\ \vdots \\ \mathbf{y}_m^{N_r} \end{pmatrix} = \begin{pmatrix} \mathbf{H}^1 \\ \vdots \\ \mathbf{H}^{N_r} \end{pmatrix} \mathbf{x}_m + \begin{pmatrix} \mathbf{n}_m^1 \\ \vdots \\ \mathbf{n}_m^{N_r} \end{pmatrix} \quad (2.3)$$

where $\mathbf{y}_m^i = [y_{m-E}^i, \dots, y_{m'}^i, \dots, y_{m+E}^i]^T$ is the received signal when the vector $\mathbf{x}_m = [x_{m-E-L+1}, \dots, x_m, \dots, x_{m+E}]^T$ is transmitted through the multipath frequency selective channel, of length L , represented by $(2E + 1) \times (2E + L)$ Toeplitz matrix $\mathbf{H}^i (i = 1, \dots, N_r)$ given by

$$\mathbf{H}^i = \begin{pmatrix} h_{L-1}^i & \dots & h_0^i & 0 & \dots & 0 \\ 0 & h_{L-1}^i & \dots & h_0^i & & \vdots \\ \vdots & & \ddots & & \ddots & 0 \\ 0 & \dots & 0 & h_{L-1}^i & \dots & h_0^i \end{pmatrix} \quad (2.4)$$

$h_l^i (0 \leq l \leq L - 1)$ are the complex coefficients of the channel connecting the transmitter antenna with the i th receiver antenna. $\mathbf{n}_m^i = [n_{m-E}^i, \dots, n_{m'}^i, \dots, n_{m+E}^i]^T$ is a vector of complex AWGN samples each has zero mean and one-sided spectral density N_0 .

3. Fast performance prediction

3.1. Prediction principle

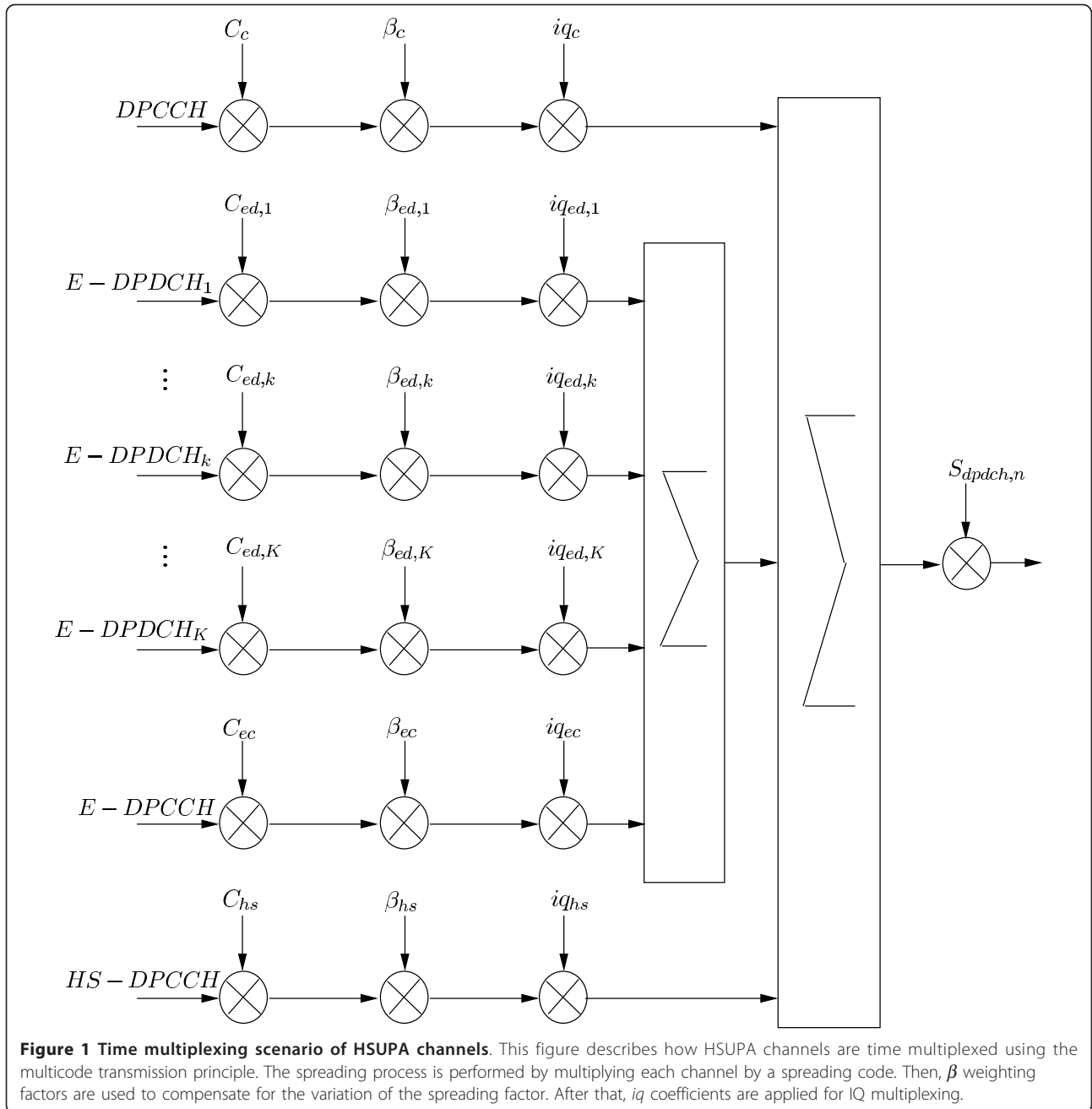
The prediction strategy principle is presented in Figure 3. The Hybrid-ARQ Chase combining approach is considered with N_t transmissions. The TPC which is necessary for HSUPA system is also taken into account. It is done separately for each Hybrid-ARQ transmission. The main hypothesis of the prediction method is the Gaussian approximation at the receiver filter output. With this assumption, the SINR at the filter output is equivalent to that of an AWGN channel. Hence, the HSUPA performance in AWGN channel can be exploited as LUT for performance abstraction in a multipath channel [7]. The abstraction accuracy depends on the SINR estimation at the filter output. Hereafter, we derive the analytical SINR expressions for both Rake and LMMSE receivers. Furthermore, an analytical SINR after Hybrid-ARQ Chase combining is given.

3.2. SINR estimation

The receiver filter output corresponding to the m th chip is expressed as

$$\begin{aligned} z_m &= \mathbf{w}^H \mathbf{y}_m \\ &= \mathbf{w}^H \mathbf{H} \mathbf{x}_m + \mathbf{w}^H \mathbf{n}_m \end{aligned} \quad (3.1)$$

where \mathbf{w} is the vector, of length $N_r(2E + 1)$, containing the receiver filter coefficients, and $(\cdot)^H$ denotes the



hermitian operator. Let us introduce a vector \mathbf{G} such that $\mathbf{G} = \mathbf{w}^H \mathbf{H} = [g_0, \dots, g_{N_r(2E)}]$. Thus, the SINR at the output of the receiver filter per Hybrid-ARQ transmission can be written as

$$\text{SINR} = \frac{SF_0 \beta_0^2 E_c |g_0|^2}{E_c (\mathbf{G}\mathbf{G}^H - |g_0|^2) + \|\mathbf{w}\|^2 N_0} \quad (3.2)$$

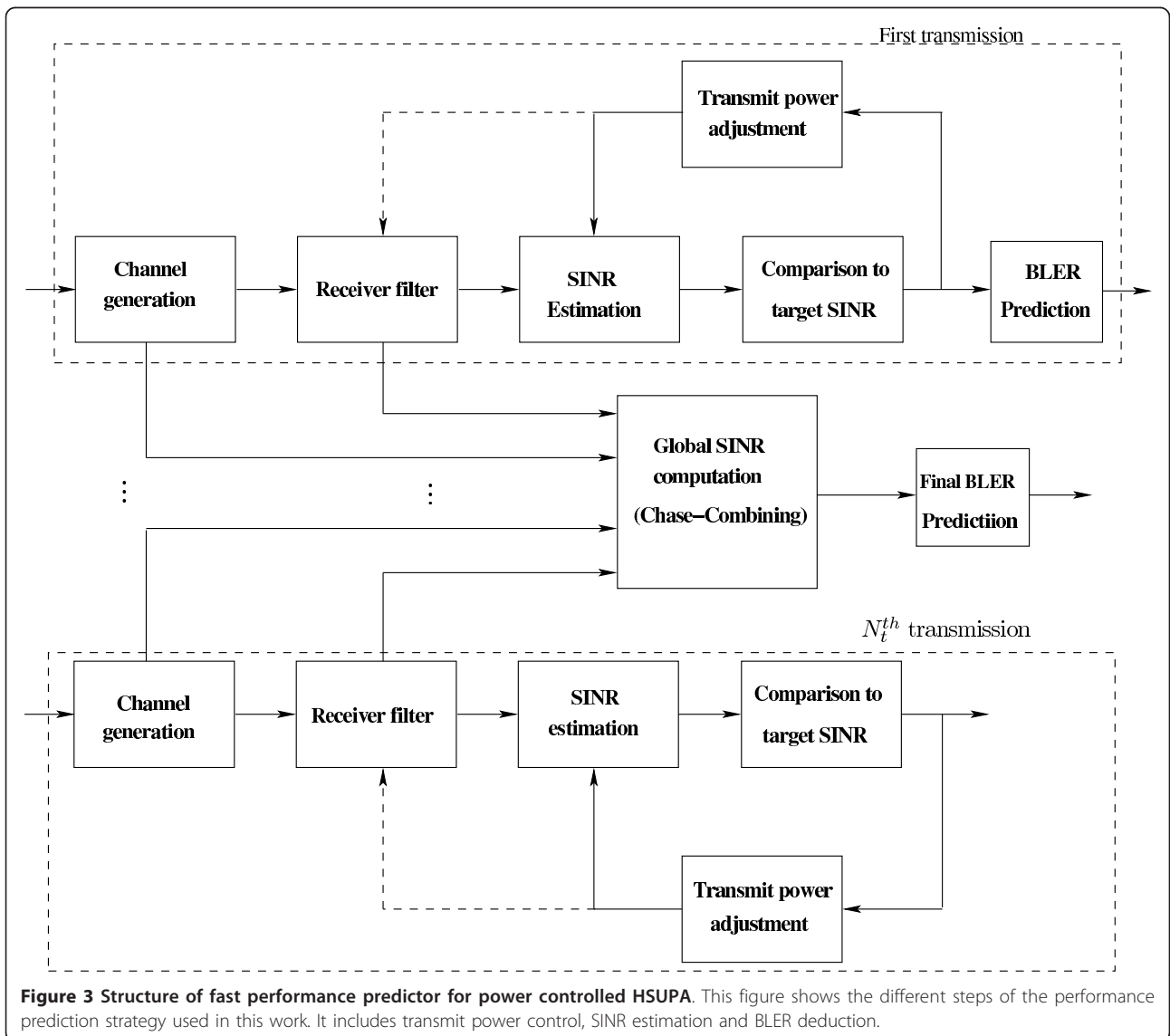
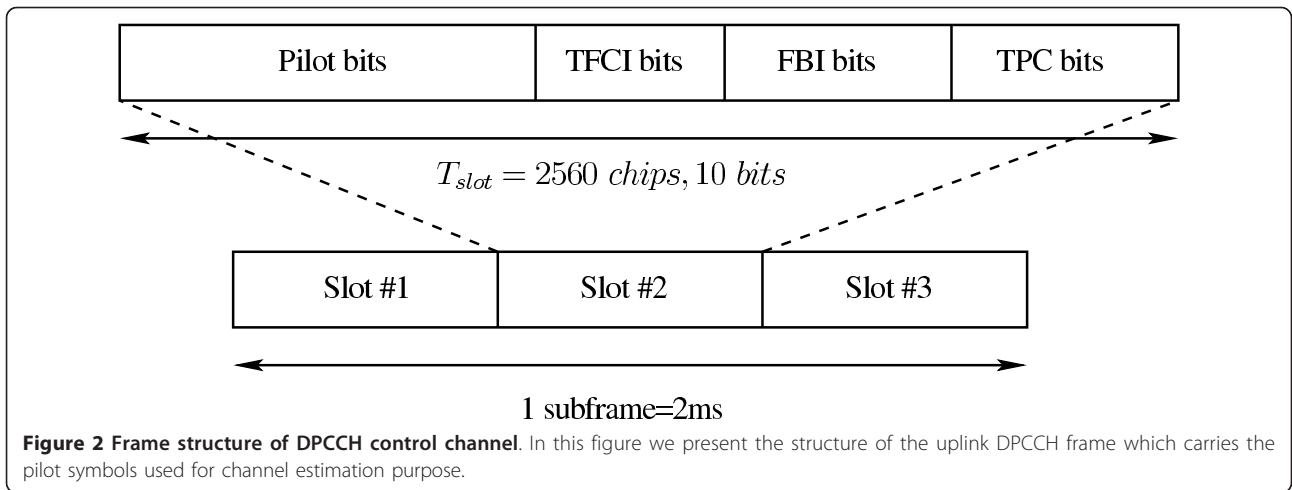
The vector of the receiver filter coefficients, \mathbf{w} depends on the selected detector. Hence, when the rake

receiver is used, this vector is expressed as

$$\mathbf{w}_{\text{rake}} = \mathbf{e}^H \mathbf{H} \quad (3.3)$$

where \mathbf{e} is a vector of zero components except the $(E + L)$ th element which is equal to 1. When the detection is done with an LMMSE detector, the vector containing its coefficients are given by

$$\mathbf{w}_{\text{LMMSE}} = \left(\mathbf{H}\mathbf{H}^H + \frac{N_0}{E_c} \mathbf{I} \right)^{-1} \mathbf{H}_{E+L} \quad (3.4)$$



where \mathbf{H}_{E+L} is the $(E + L)$ th column of channel matrix \mathbf{H} .

Once the SINR is computed according to (3.2), it is compared to a target SINR, SINR_T . The TPC commands are then generated as follows:

$$\text{TPC}_{cmd} = \begin{cases} "0", & \text{if } \text{SINR} > \text{SINR}_T \\ "1", & \text{if } \text{SINR} < \text{SINR}_T \end{cases} \quad (3.5)$$

where *cmd* refers to command.

After TPC commands generation, the transmit chip power is adjusted (see Figure 3) in response to these commands: if $\text{TPC}_{cmd} = "0"$, the transmit power is reduced by δ dB which is the power control step size. When $\text{TPC}_{cmd} = "1"$ the transmit power is increased by δ dB. This is done in an iterative manner until the target SINR is reached. At the first transmission, after power control, the SINR is also used in Hybrid-ARQ process. Its availability makes it easy to predict the BLER which indicates if a packet is correctly received or erroneous. If a packet is detected to be in error, its retransmission is requested, and the channel matrix and the filter coefficients are saved for Chase combining SINR computation.

Let us consider Chase combining scenario with N_t transmissions. The output of Hybrid-ARQ combiner may be written as

$$\mathbf{z} = \sum_{j=0}^{N_t-1} \mathbf{z}_j \quad (3.6)$$

where \mathbf{z}_j is the filter output corresponding to the j th Hybrid-ARQ transmission.

If we introduce a vector $\mathbf{G}_j = \mathbf{w}^H \mathbf{H}_j$, Equation (3.6) can be then expressed in the following vector form

$$\begin{aligned} \mathbf{z} &= \left(\sum_{j=0}^{N_t-1} \mathbf{w}^H \mathbf{H}_j \right) \mathbf{x} + \left(\sum_{j=0}^{N_t-1} \mathbf{f}_j^H \mathbf{n}_j \right) \\ &= \left(\sum_{j=0}^{N_t-1} \mathbf{G}_j \right) \mathbf{x} + \left(\sum_{j=0}^{N_t-1} \mathbf{w}_j^H \cdot \mathbf{n}_j \right) \end{aligned} \quad (3.7)$$

Consequently, the global SINR after the despreading is calculated according to this formula

$$\text{SINR}_{global} = \frac{SF_0 \beta_0 E_c \left| \sum_{j=0}^{N_t-1} g_{0j} \right|^2}{E_c \left(\left(\sum_{j=0}^{N_t-1} \mathbf{G}_j \right) \left(\sum_{j=0}^{N_t-1} \mathbf{G}_j \right)^H - \left| \sum_{j=0}^{N_t-1} g_{0j} \right|^2 \right) + \left(\sum_{j=0}^{N_t-1} \mathbf{w}_j \mathbf{w}_j^H \right) N_0} \quad (3.8)$$

After SINR estimation, the BLER is derived from pre-stored LUTs which summarize the iterative turbo decoding behavior. These LUTs are constructed using Monte-Carlo simulations through an AWGN channel, and are in the form $(E_c/N_0$ in dB, BLER). So, it is

necessary to compute an effective E_c/N_0 from the estimated SINR. For this, we assume that the SINR at the output of the receiver filter is equivalent to the received E_b/N_0 . Thus, the effective E_c/N_0 can be calculated as follows

$$\left(\frac{E_c}{N_0} \right)_{dB} = (\text{SINR})_{dB} - 10 \log_{10} (\beta_{ed,1}^2 SF_{ed,1}) \quad (3.9)$$

The BLER is then obtained according to this expression

$$\text{BLER} = f \left(\left(\frac{E_c}{N_0} \right)_{dB} \right), \quad (3.10)$$

where $f(\cdot)$ indicates the linear interpolation function which takes the effective $(\frac{E_c}{N_0})_{dB}$ as input and calculates the corresponding BLER by linear interpolation using AWGN LUT [8].

4. Effect of channel estimation on the performance prediction

When perfect channel knowledge is not considered, the receiver has to make channel estimation which cannot be done without error. This affects the performance prediction methodology since the SINR estimation is based on the estimated channel coefficients. In this study, channel estimation is performed by extracting the channel state information contained in the known pilot symbols carried by the UMTS control channel DPCCCH [9,10]. So, DPCCCH despreading is necessary for channel estimation. As in [11], under the hypothesis that the channel estimation error has a Gaussian behavior, we can express the estimated channel vector, $\hat{\mathbf{h}} = [\hat{h}_0, \dots, \hat{h}_{L-1}]$ as

$$\hat{\mathbf{h}} = \mathbf{h} + \mathbf{u}, \quad (4.1)$$

where \mathbf{u} is a vector of L AWGN samples, each of zero mean, and a variance expressed (see Appendix 1) as

$$\sigma_u^2 = \frac{1}{N_p E_{c,pilot} SF_{pilot}} \left(\left(\sum_{l \neq i}^L |h_l|^2 \right) E_c + \frac{N_0}{2} \right) \quad (4.2)$$

The SINR at the Rake receiver output is given by

$$\text{SINR}_{\text{Rake}}(\hat{\mathbf{h}}/\mathbf{h}) = \frac{SF_1 \beta_1^2 E_c \left| \hat{\mathbf{h}} \mathbf{H}_{(E+L)} \right|^2}{E_c \left(\hat{\mathbf{h}}^H \mathbf{H} \mathbf{H}^H \hat{\mathbf{h}} - \left| \hat{\mathbf{h}} \mathbf{H}_{(E+L)} \right|^2 \right) + \left\| \hat{\mathbf{h}} \right\|^2 N_0} \quad (4.3)$$

If we denote by $\hat{\mathbf{H}}$ the estimated channel matrix, the vector of LMMSE filter coefficients is written as

$$\hat{\mathbf{w}}_{\text{LMMSE}} = \left(\hat{\mathbf{H}} \hat{\mathbf{H}}^H + \frac{N_0}{E_c} \mathbf{I} \right)^{-1} \hat{\mathbf{H}}_{E+L} \quad (4.4)$$

Define the vector $\hat{\mathbf{G}} = \hat{\mathbf{w}}_{\text{LMMSE}}^H \mathbf{H}$. The SINR at the output of the LMMSE filter is given by

$$\text{SINR}_{\text{LMMSE}}(\hat{\mathbf{h}}/\mathbf{h}) = \frac{SF_0 \beta_0^2 E_c |\hat{g}_0|^2}{E_c (\hat{\mathbf{G}} \hat{\mathbf{G}}^H - |\hat{g}_0|^2) + \|\hat{\mathbf{w}}\|^2 N_0} \quad (4.5)$$

Performance prediction process with channel estimation is accomplished according to the following steps.

- Step 1. Draw a channel realization \mathbf{h} ,
- Step 2. Generate the noisy channel, $\hat{\mathbf{h}}$ such as $\hat{\mathbf{h}} = \mathbf{h} + \mathbf{u}$, where \mathbf{u} is the vector of AWGN noise samples where each has zero mean and the variance expressed in (4.2),
- Step 3. Compute $\text{SINR}(\hat{\mathbf{h}}/\mathbf{h})$ using the formula in (4.3) for the Rake receiver, or (4.5) for the LMMSE detector,
- Step 4. Calculate the mathematical expectation of $\text{SINR}(\hat{\mathbf{h}}/\mathbf{h})$,
- Step 5. Perform the TPC process
 - Compare $\text{SINR}(\hat{\mathbf{h}}/\mathbf{h})$ to the target SINR, SINR_T
 - Update the transmitted chip energy, E_c , using TPC command as expressed in (4.6)
 - Calculate the $\text{SINR}(\hat{\mathbf{h}}/\mathbf{h})$ with updated E_c
- Step 6. Predict the BLER using LUTs and the final SINR corresponding to current transmission.

Note that TPC process (Step 5) is repeated until the estimated $\text{SINR}(\hat{\mathbf{h}}/\mathbf{h})$ is close to target SINR. Then, the final value of SINR is used for Hybrid-ARQ Chase combining purpose.

To see whether a retransmission of a packet is required or not, the predicted BLER in Step 6 is exploited and compared to a uniform random variable ϑ , in the following criterion

$$\begin{cases} \text{ACK,} & \text{if BLER} \geq \vartheta \\ \text{NACK,} & \text{elsewhere} \end{cases} \quad (4.6)$$

5. Numerical results

In this section, simulation results are presented to investigate the effect of channel estimation on the FPP technique. First, we describe how the LUTs are built. The FPP is then validated with the HSUPA link simulator (LS) according to 3GPP technical specifications [12]. The joint application of Chase combining and transmit power control is also addressed. Finally, simulation results of FPP with channel estimation are presented for HSUPA configurations presented in Table 1.

Table 1 HSUPA modulation and coding schemes

MCS	Number of codes	Min. SF	Coding rate	TTI (ms)	Max. bit rate (kbps)
1	1	16	0.288	10	69.0
2	2	2	0.502	10	1927.0
3	2	2	0.702	2	2706.0

This table summarizes HSUPA configurations used in this study.

5.1. Look-up tables

As mentioned above, the LUTs are built using the HSUPA LS with an AWGN channel. The Max-Log-MAP algorithm, with 8 iterations, is considered for decoding the 1/3 rate Turbo encoder. The LUTs summarize the HSUPA decoder behavior in terms of BLER as a function of received E_c/N_0 . Each selected HSUPA modulation and coding scheme (MCS) needs only one LUT, also called a reference curve, for its performance prediction in a multipath channel.

5.2. Fast performance prediction validation

To see how accurate the FPP simulator is, computer simulations are done for HSUPA transmission. The FPP performance are obtained using the simulator corresponding to the structure presented in Figure 3 which consider hybrid-ARQ Chase combining [13,14]. The TPC process is assumed to be inactive. For FPP verification, it is necessary to run the HSUPA link simulator and FPP simulator with the same assumptions. In the LS, the turbo decoding is performed using the same algorithm and number of iterations as those used for building the LUTs. However, in the FPP simulator, turbo decoding process is not run since its performance is summarized in LUTs.

Simulations were done for MCS 2 through a multipath channel which has the same profile as ITU-Pedestrian B Channel with the mobile speed of 3 Km/h. Note that profile is used for all computer simulations in this work. Results of FPP and LS simulator, are shown in Figure 4. It is observed that the FPP simulator is accurate compared to the LS for both single transmission and Hybrid-ARQ retransmissions. Moreover, with FPP, a significant gain, in terms of CPU time is obtained.

5.3. Channel estimation effect on FPP

Having shown the accuracy of FPP simulator, computer simulations are run to assess the impact of noisy channel on this simulator considering Hybrid-ARQ Chase combining and TPC. The HSUPA configuration we adopted in this assessment includes MCS 1 and MCS 3. The number of Hybrid-ARQ transmissions is fixed to 2. The detection is done with a Rake receiver for the first configuration. The second one (MCS 3) is detected with

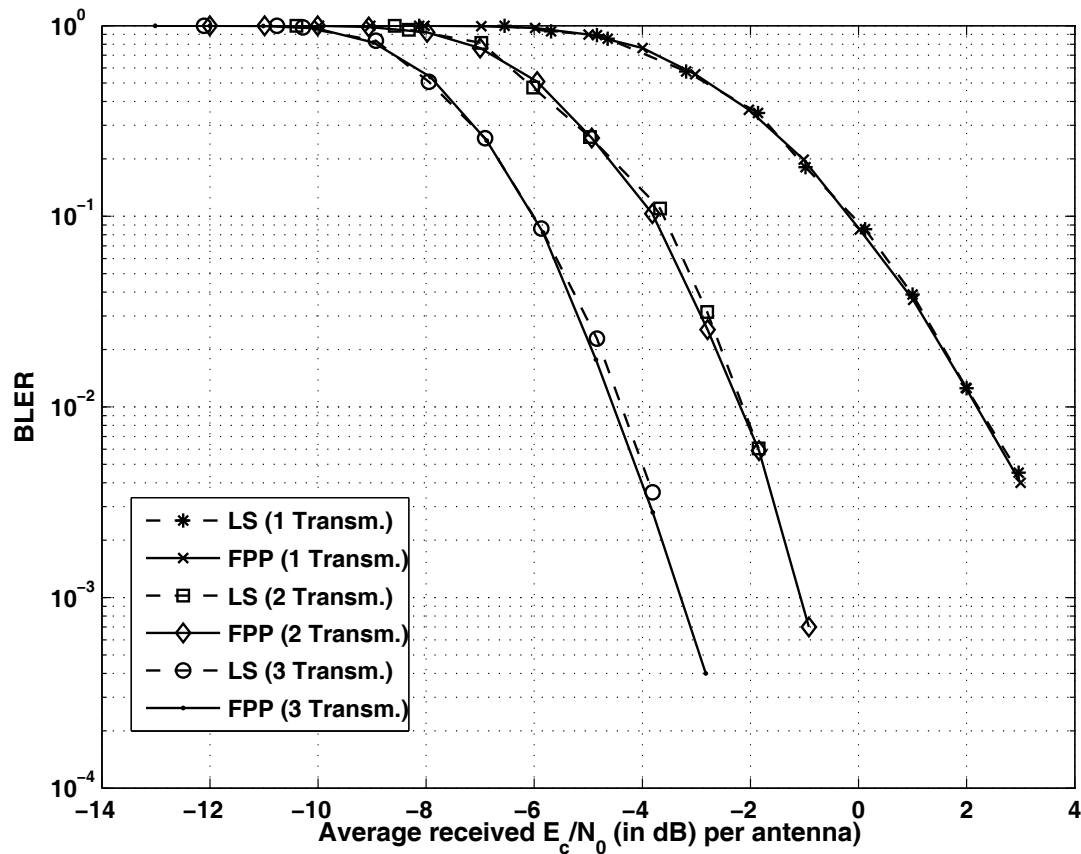


Figure 4 Validation of FPP with MCS 2. In this figure we show how accurate is our proposed prediction strategy in terms of BLER. The results presented in this figure are given for MCS 2 configuration (see Table 1).

LMMSE equalizer. As mentioned in 3GPP technical specifications [9], the maximum number of pilot symbols, N_{pilot} , is fixed to 150 for MCS 1 and 30 for MCS 3.

BLER results of MCS 1 are presented in Figure 5. It is seen that for received E_c/N_0 lower than -19 dB, no enhancement is provided by the TPC process when perfect channel knowledge is assumed at the receiver end. However, when received E_c/N_0 is up to -19 dB, an important gain is offered. In fact, we observed a gap of 2.5dB when a BLER of 10^{-2} is reached. If channel estimation is taken into consideration, this gain depends on the number of pilot symbols used to estimate the channel coefficients. A great number of pilots results in more accurate estimated channel coefficients. It is also seen that the noisy channel effect is considerable for a small number of pilot symbols. This effect is significantly reduced after applying TPC.

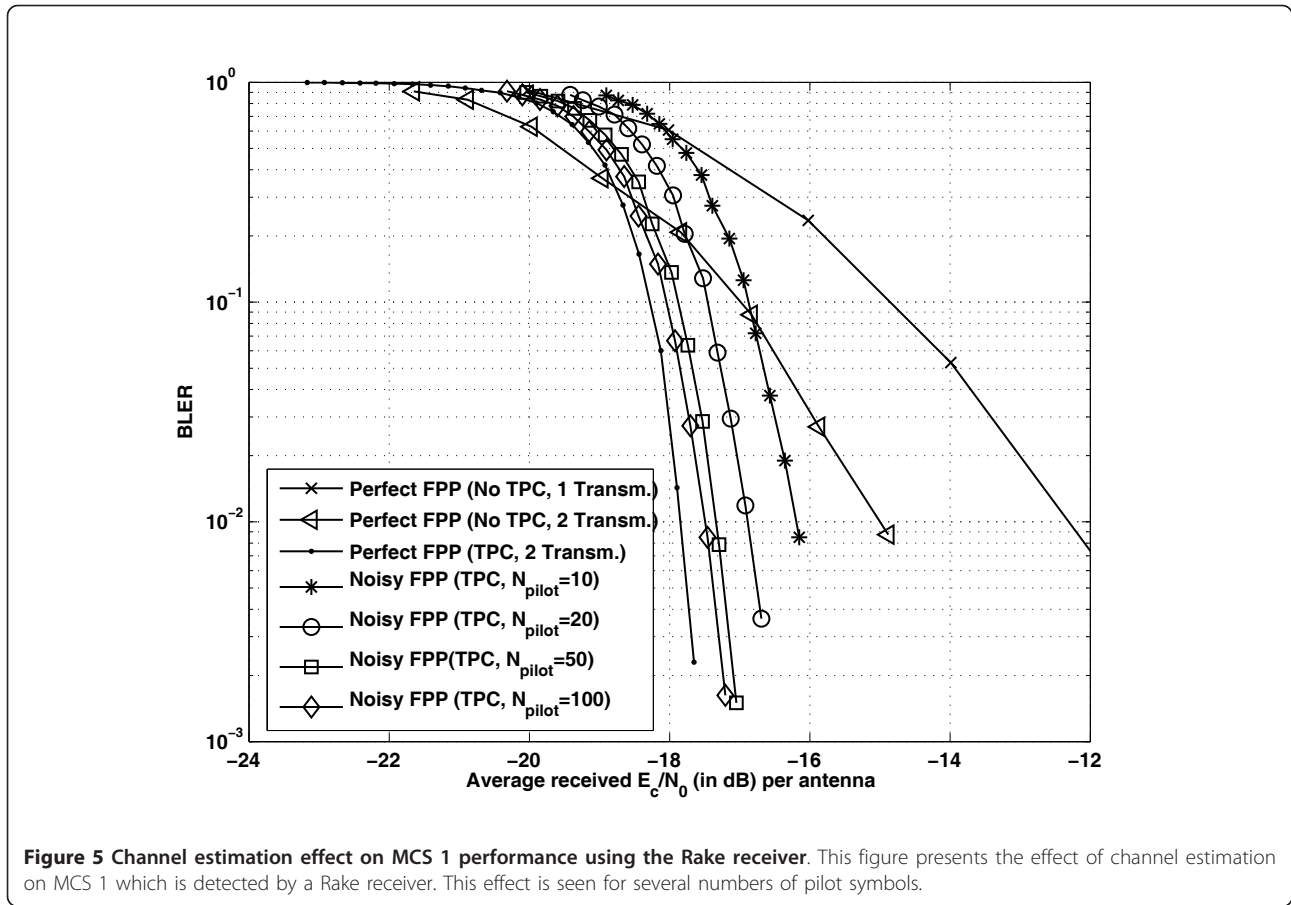
Figure 6 presents the BLER results of MCS 3 which uses LMMSE equalizer. It is concluded that the system

exhibits poor performance for lowest number of pilot symbols.

The channel estimation effect, in terms of transmitted pilot symbols, may be seen using the empirical cumulative density function (CDF) of parameter Delta which is defined to be the difference between the SINR computed assuming perfect channel knowledge and that calculated with noisy channel coefficients. Figure 7 shows the CDF of Delta for different numbers of pilot symbols. It is also seen, in terms of CDF that the impact of channel estimation on fast simulator is significantly reduced when increasing the number of pilot symbols.

6. Conclusion

In this article, we have studied the effect of channel estimation on the simplified simulation methodology for HSUPA. Transmit power control and Hybrid-ARQ Chase combining have been considered. It has been seen that a noisy channel effect depends on the number



of pilot symbols transmitted on the HSUPA control channel. Simulation results have demonstrated that when a sufficient number of these symbols is used, the performance is close to that obtained with the perfect CHACK assumption. Moreover, the application of TPC compensates the performance degradation when a small number of pilot symbols. This has been verified for both Rake and LMMSE receivers.

Appendix 1: DPCCH despreading

For channel coefficients estimation in HSUPA, we have to extract the time multiplexed control channel, DPCCH, from received signal containing the pilot symbols. For this, we consider the HSUPA transmission scenario presented in Figure 1. The multicode transmitted signal with time multiplexed control channels is expressed as follows

$$\begin{aligned}
 x_{\text{hsupa}}(t) = & \sqrt{E_c} \sum_{k=0}^{K-1} s_{ed,k}(t) c_{ed,k}(t) \beta_{ed,k} \cdot iq_{ed,k} \\
 & + \sqrt{E_c} S_{ec}(t) C_{ec}(t) \beta_{ec} \cdot iq_{ec} \\
 & + \sqrt{E_c} S_{hs}(t) C_{hs}(t) \beta_{hs} \cdot iq_{hs} + \sqrt{E_c} S_c(t) C_c(t) \beta_c \cdot iq_c
 \end{aligned} \tag{6.1}$$

After time multiplexing, the multicode signal is sent through a multipath channel which has the well known impulse response given by this expression

$$h_c(t) = \sum_{l=0}^{L-1} h_l \delta(t - \tau_l) \tag{6.2}$$

The received signal is then expressed as

$$\begin{aligned}
 x_{\text{hsupa}}(t) = & x_{\text{hsupa}}(t) * h_c(t) + n(t) \\
 = & \sqrt{E_c} \sum_{l=0}^{L-1} h_l \sum_{k=0}^{K-1} s_{ed,k}(t - \tau_l) c_{ed,k}(t - \tau_l) \beta_{ed,k} \cdot iq_{ed,k} \\
 & + \sqrt{E_c} S_{ec} \beta_{ec} \cdot iq_{ec} \sum_{l=0}^{L-1} h_l C_{ec}(t - \tau_l) \\
 & + \sqrt{E_c} \sum_{l=0}^{L-1} h_l (S_{hs} \beta_{hs} \cdot iq_{hs} C_{hs}(t - \tau_l) \\
 & + \sqrt{E_c} S_c \beta_c \cdot iq_c C_c(t - \tau_l)) + n(t)
 \end{aligned} \tag{6.3}$$

DPCCH despreading is performed by calculating the inter-correlation between the received multicode signal and the spreading code corresponding to DPCCH

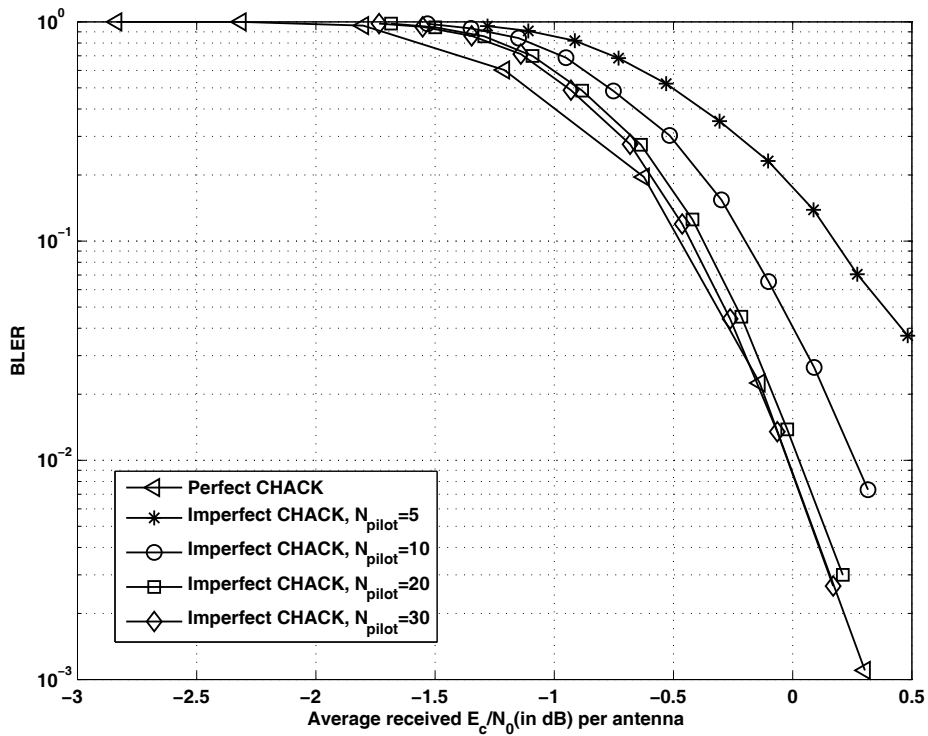


Figure 6 Channel estimation effect on MCS 3 performance using the LMMSE chip equalizer. Figure 6 shows the channel estimation effect on MCS 3 performance. The detection for this configuration is done with LMMSE chip equalizer.

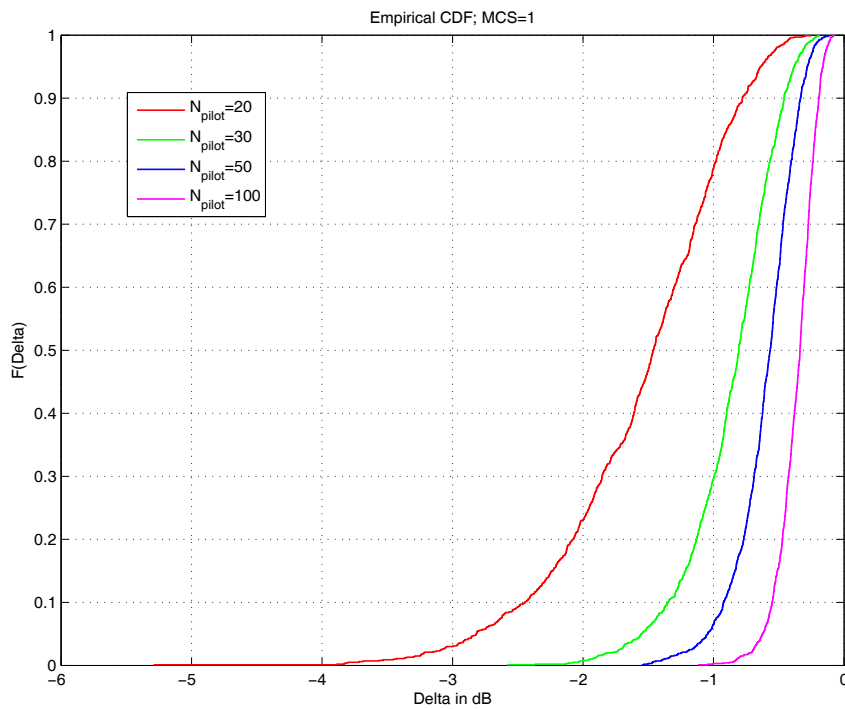


Figure 7 Impact of channel estimation on MCS 1 performance in terms of CDF. In Figure 7, we present the channel estimation effect on MCS 1 in terms of cumulative distribution function.

channel. The output of the despreading process is written as

$$v_l(t) = \frac{1}{SF_{\text{pilot}}} \sqrt{E_c \beta_c} i q_c S_c \sum_{l=0}^{L-1} h_l + n_l(t) \quad (6.4)$$

where $n_l(t)$ is the noise after despreading. The l th channel coefficient is estimated by multiplying the despreading output by the transmitted pilot sequence. This is expressed as

$$\tilde{h}_l = \frac{1}{SF_{\text{pilot}}} \sqrt{E_c \beta_c} i q_c \sum_{l=0}^{L-1} h_l + n_l(t) S_c \quad (6.5)$$

Since the additive noise is an AWGN with zero mean (by simulation), its effect on channel estimation can be significantly reduced by taking an average of \tilde{h}_l over the pilot sequence size, N_p .

$$\hat{h}_l = \frac{1}{SF_{\text{pilot}} N_p} \sqrt{E_c \beta_c} \sum_{l=0}^{L-1} h_l + \frac{1}{N_p} \sum_{j=0}^{N_p-1} n_l(t) S_{c,j} \quad (6.6)$$

Acknowledgements

Many thanks to our TEX-pert for developing this class file.

Author details

¹Department of Communications Systems, INPT, 2 Av. Allal El Fassi, Madinat Al Irfane, Rabat, Morocco ²Lab-STICC, UMR CNRS 3192/Ueb, Signal and Communications Department, Institut Telecom/Telecom Bretagne, Technopôle, Brest-Iroise, CS 83818, 29200 Brest Cedex 3, France ³Orange-Labs, 38-40 rue du générale-Leclerc, F-92792 Issy Moulineaux, Cedex 9, France

Competing interests

The authors declare that they have no competing interests.

Received: 2 June 2011 Accepted: 19 April 2012 Published: 19 April 2012

References

1. M Et tolba, S Saoudi, R Visoz, TA Idir, Chip-level LMMSE based Hybrid-ARQ Chase combining for HSUPA, in *IEEE VTC-Spring*, vol. 3. Barcelona, Spain, pp. 1-5 (2009)
2. J Kim, A Ashikhmin, AJ van Wijngaarden, E Sljanin, N Gopalakrishnan, *On Efficient Link Error Prediction based on Convex Metrics*, vol. 6. IEEE VTC-Fall, Los Angeles, pp. 4190-4194 (2004)
3. TSG-RAN WG1 #35, *Link Error Prediction for E-DCH*, (Lucent Technologies, Lisbon, 2003)
4. M Et tolba, M Eljamai, S Saoudi, R Visoz, Link performance prediction for HSUPA in a multipath channel, in *ACM IWCMC'09*, vol. 2. Leipzig, Germany, pp. 859-863 (2009)
5. M Lalam, R Visoz, AO Berthet, Fast performance prediction of iterative MMSE-IC receivers in MU-MIMO CDMA systems, in *IEEE VTC-Fall*, vol. 6. Anchorage, Alaska, pp. 1-5 (2009)
6. I Stupia, F Gianneti, V Lottici, L Vandendorpe, A novel link performance prediction method for coded MIMO-OFDM systems, in *IEEE WCNC*, vol. 1. Budapest, pp. 1-5 (2009)
7. A Das, A Sampath, Link error prediction for wireless system simulations, in *IEEE WCNC*, vol. 1. Atlanta, Georgia USA, pp. 507-512 (2004)
8. R Ratasuk, A Ghosh, T Brown, R Love, W Xiao, Predicting link level performance for enhanced uplink, in *IEEE VTC-Fall*, vol. 6. Los Angeles, pp. 4185-4189 (2004)
9. 3GPP TS 25.213, *Spreading and Modulation (FDD)* (May 2007). Version 7.2.0

10. H Holma, A Toskala, *HSDPA/HSUPA for UMTS*, (John Wiley and Sons Ltd, The Atium Southern Gate, Chichester, West Sussex PO 19 8SQ, England, 2006)
11. R Visoz, AO Berthet, M Lalam, A novel fast semi-analytical performance prediction method for iterative MMSE-IC multiuser MIMO joint decoding, in *IEEE GLOBECOM*, vol. 55. New Orleans, LA, USA, pp. 1-5 (2008)
12. S Parkvall, J Paisa, J Torsner, M Sagfors, P Malm, WCDMA enhanced uplink - principles and basic operation, in *IEEE VTC-Spring*, vol. 3. Stockholm, Sweden, pp. 1411-1415 (2005)
13. D Chase, Code Combining-a maximum-likelihood decoding approach for combining an arbitrary number of noisy packets. *IEEE Trans Commun.* XXXIII, 385-393 (1985)
14. M Bertinelli, E Malkamaki, Hybrid-ARQ for WCDMA enhanced uplink: link level performance in SHO, in *IEEE PIMRC*, vol. 4. Barcelona, Spain, pp. 2856-2860 (2004)

doi:10.1186/1687-1499-2012-148

Cite this article as: Et-tolba et al.: Fast performance prediction of power controlled HSUPA with channel estimation. *EURASIP Journal on Wireless Communications and Networking* 2012 **2012**:148.

Submit your manuscript to a SpringerOpen® journal and benefit from:

- Convenient online submission
- Rigorous peer review
- Immediate publication on acceptance
- Open access: articles freely available online
- High visibility within the field
- Retaining the copyright to your article

Submit your next manuscript at ► springeropen.com